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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RESPONSE AFTER FINAL
EXPEDITED PROCEDURE
EXAMINING GROUP 2816

AP
2800

In re application of:

SATO

Attorney Docket No: 108397-00042

Serial Number: 09/843,820

Group Art Unit: 2816

#14/E

Filed: April 30, 2001

Examiner: A. Luu

Arnold
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4/3/03
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For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING CIRCUIT FOR
CHANGING TIMING OF INACTIVATING POWER-ON RESETTING CIRCUIT

AMENDMENT AND RESPONSE UNDER 37 C.F.R. § 1.116

Commissioner for Patents
Washington, D.C. 20231

March 28, 2003

Sir:

In reply to the Office Action dated January 28, 2003, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend claim 1 as follows:

1. (Four Times Amended) A semiconductor integrated circuit comprising:

a power-on resetting circuit for activating a reset signal in response to an initial supply of a power supply to initialize an internal circuit, and for inactivating the reset signal after a predetermined period following the initial supply to terminate an initialization of the internal circuit; and

a timing changing circuit for adjusting the predetermined period in accordance with an internal signal generated in an interior of said timing changing circuit.

A marked-up version of the amended claims is enclosed as required by 37 C.F.R. § 1.121.

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